

FIG. 1

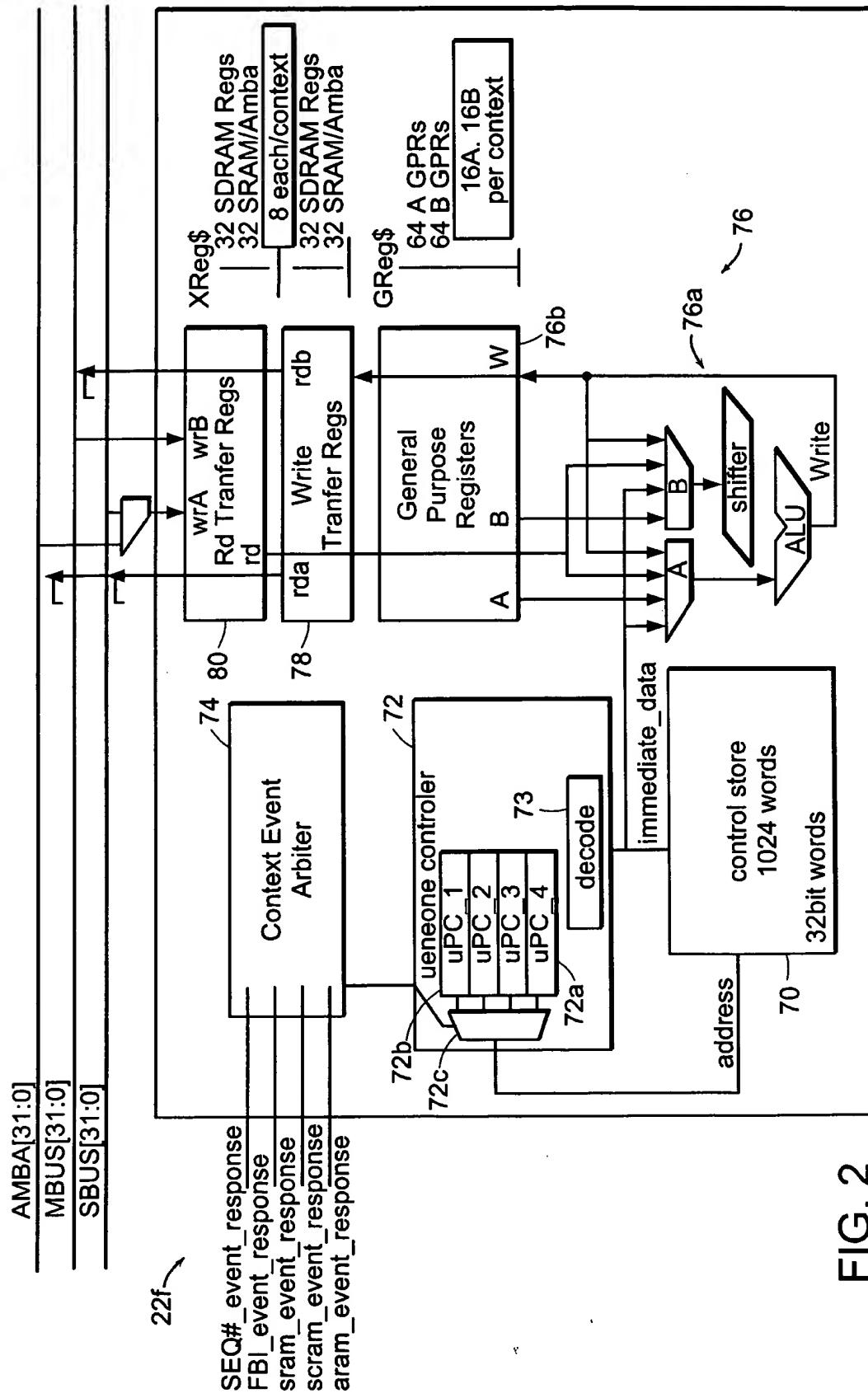


FIG. 2

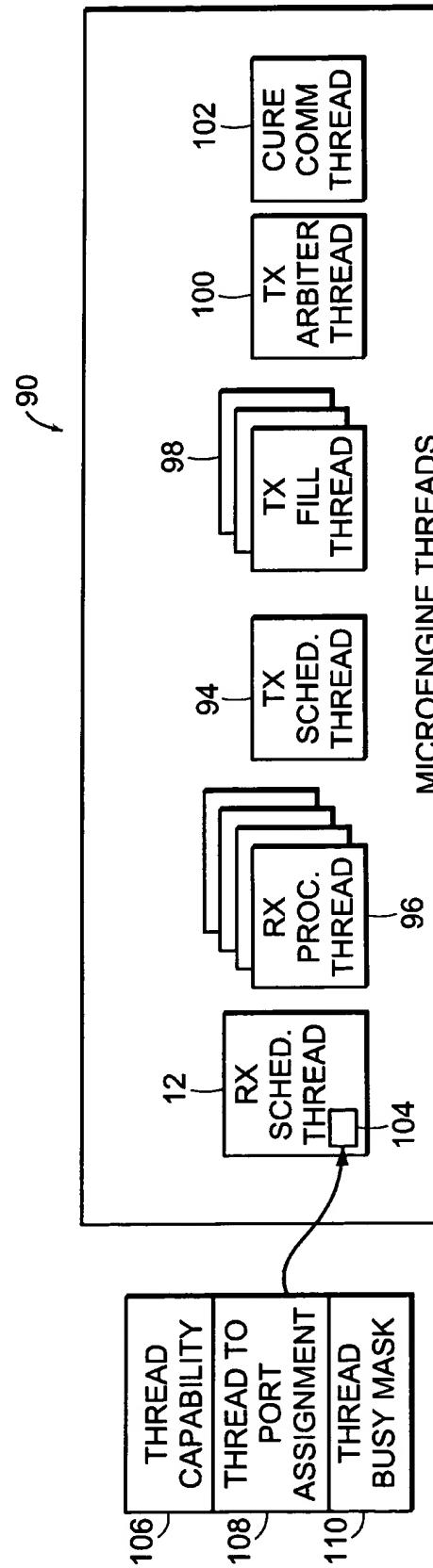


FIG. 3



Applicant(s): Robert W. Chen et al.
**PORT BLOCKING TECHNIQUE FOR MAINTAINING RECEIVE
PAKET ORDERING FOR A MULTIPLE ETHERNET PORT
SWITCH**

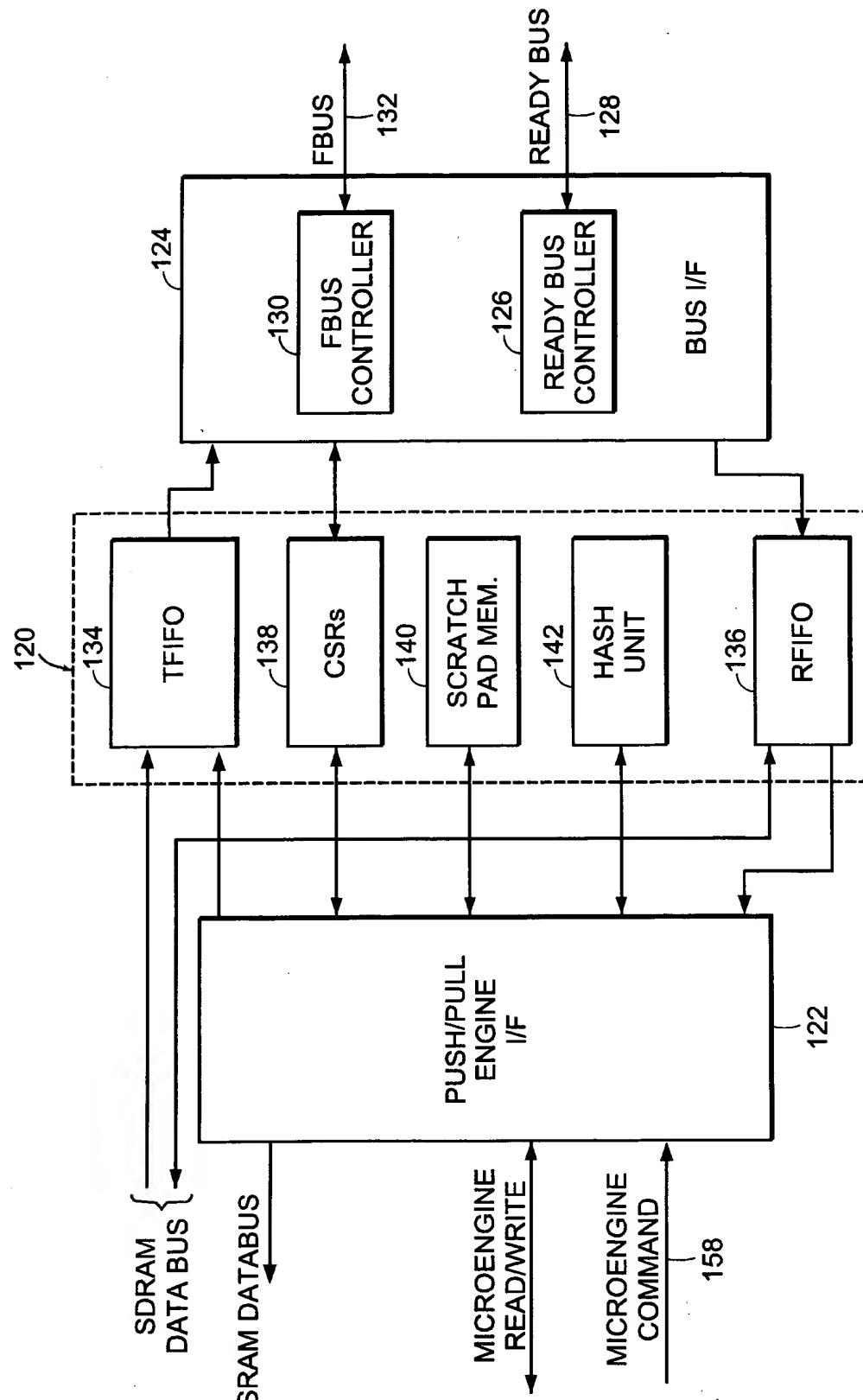


FIG. 4

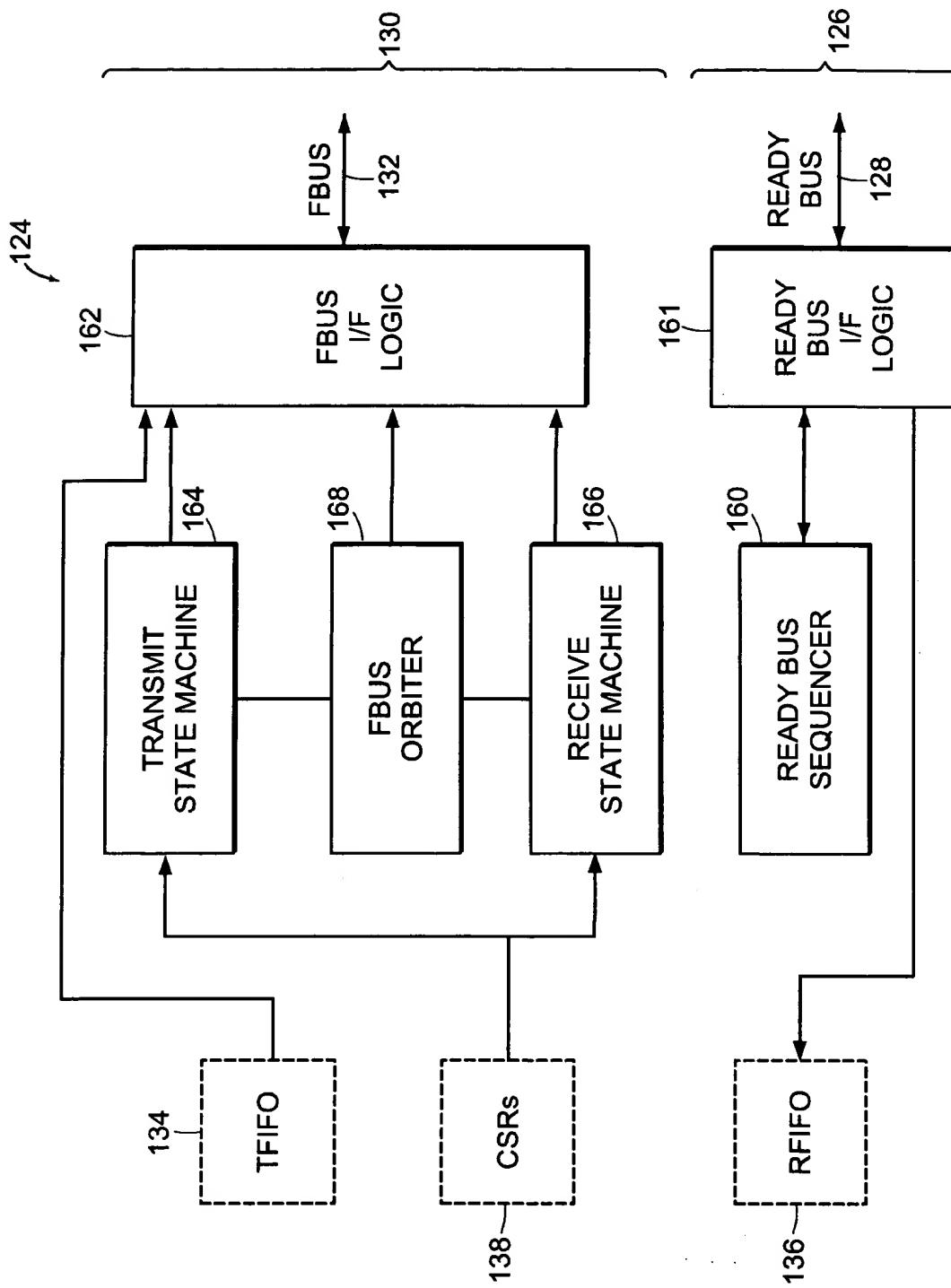


FIG. 5



Appln No.: 09/176,303
Applicant(s): Robert Wolrich et al.

Page 6 of 12

PORT BLOCKING TECHNIQUE FOR MAINTAINING RECEIVE
PAKET ORDERING FOR A MULTIPLE ETHERNET PORT
SWITCH

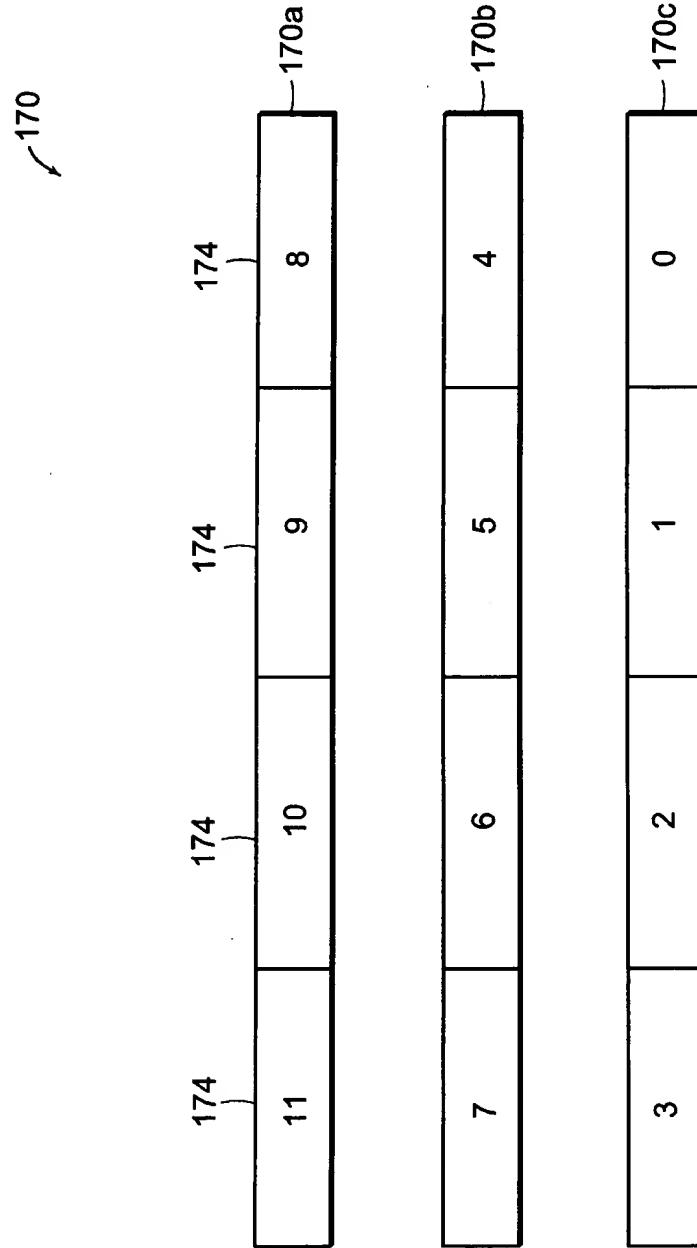


FIG. 6A



Appln No. 09/476,303

Page 7 of 22

Applicant: Gilbert Wolrich et al.

PORT BLOCKING TECHNIQUE FOR MAINTAINING RECEIVE
PAKET ORDERING FOR A MULTIPLE ETHERNET PORT
SWITCH

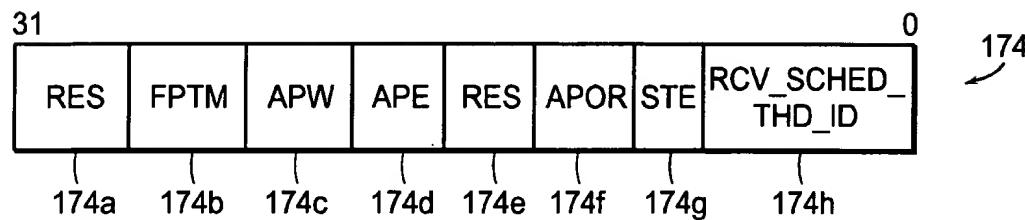


FIG. 6B



FIG. 6C

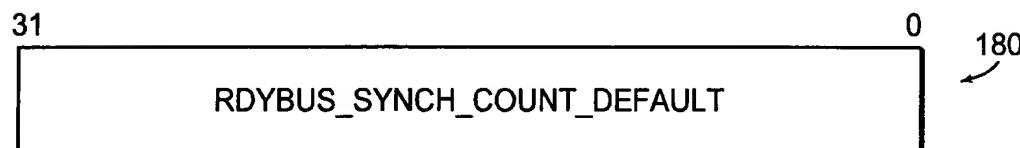


FIG. 6D

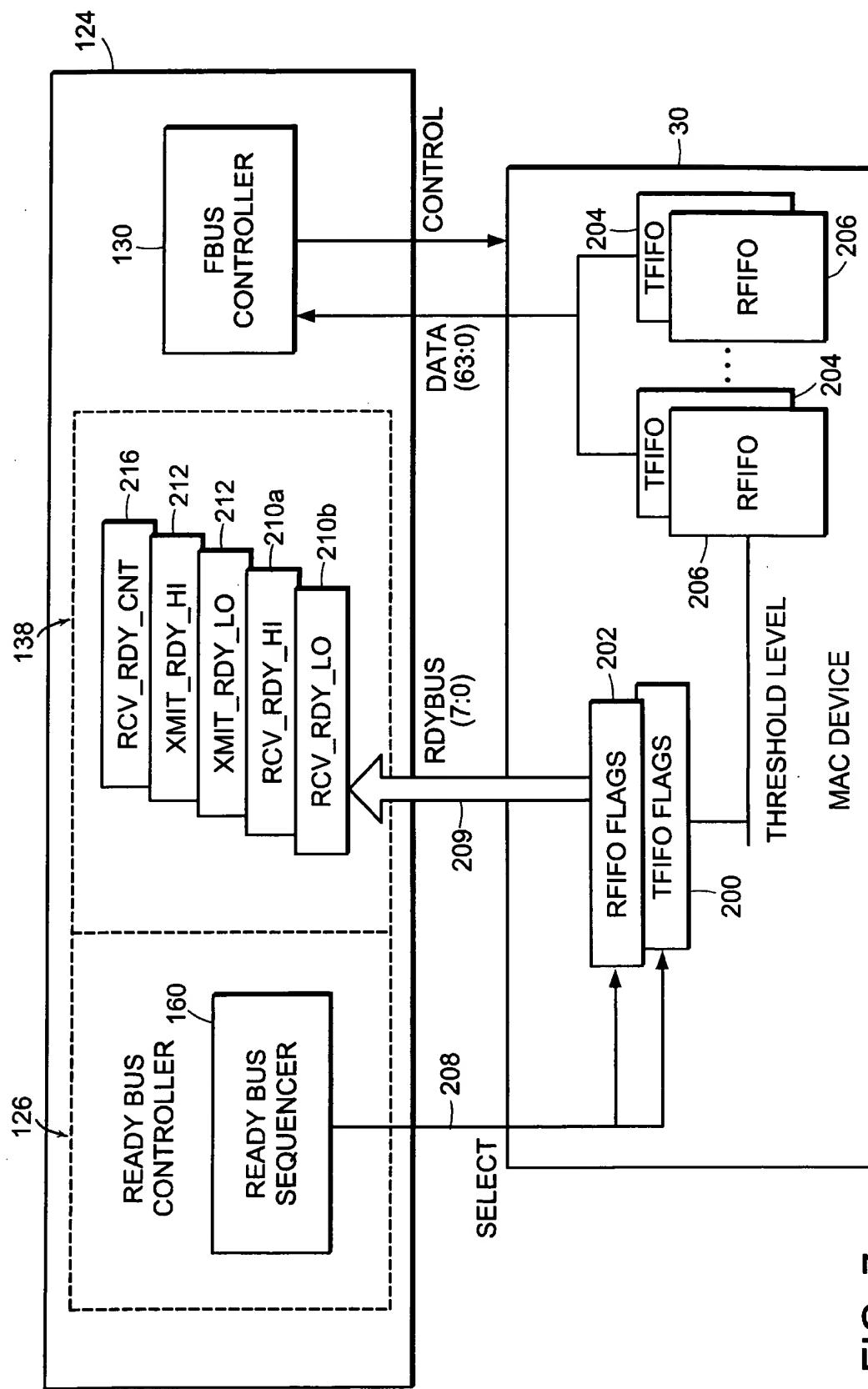


FIG. 7

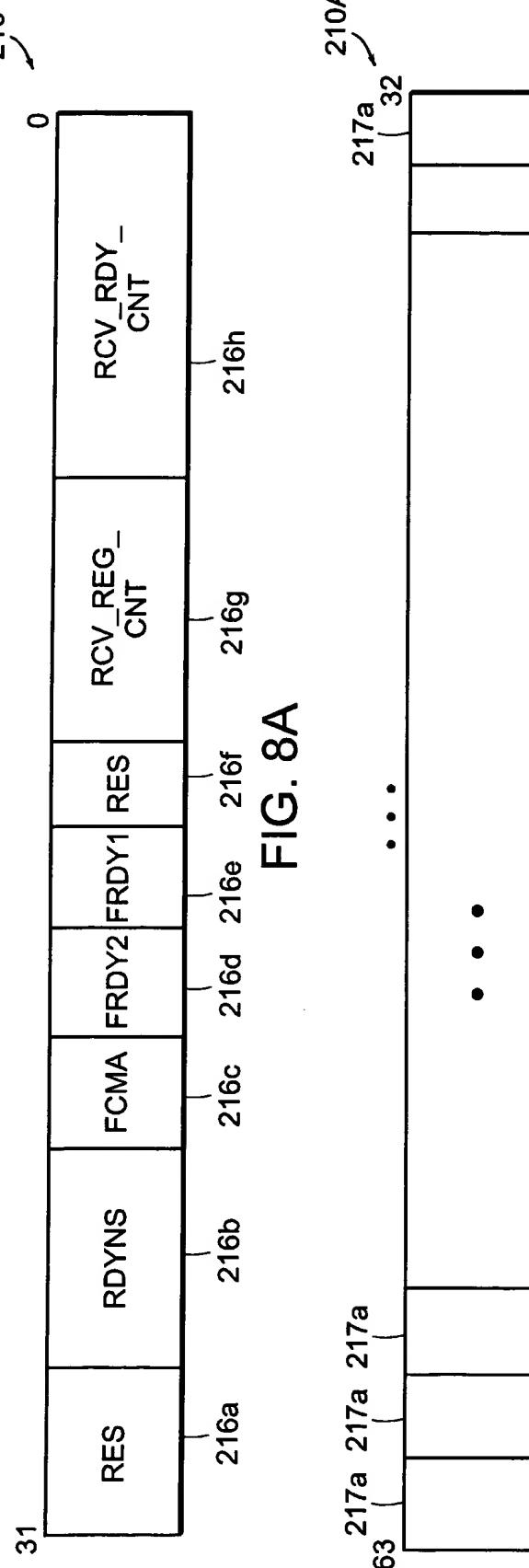
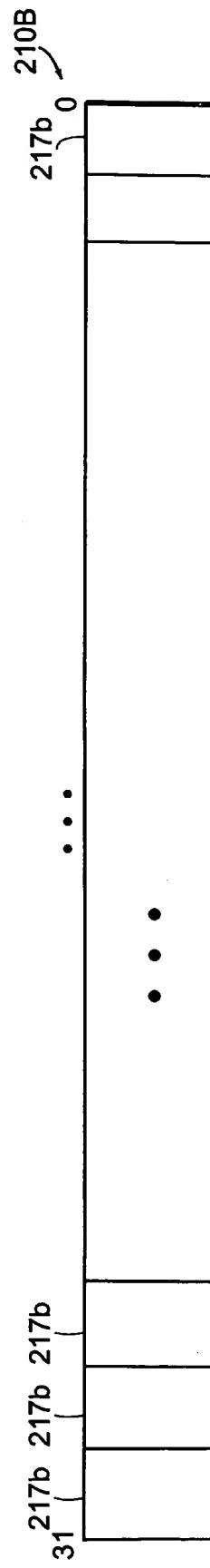


FIG. 8B



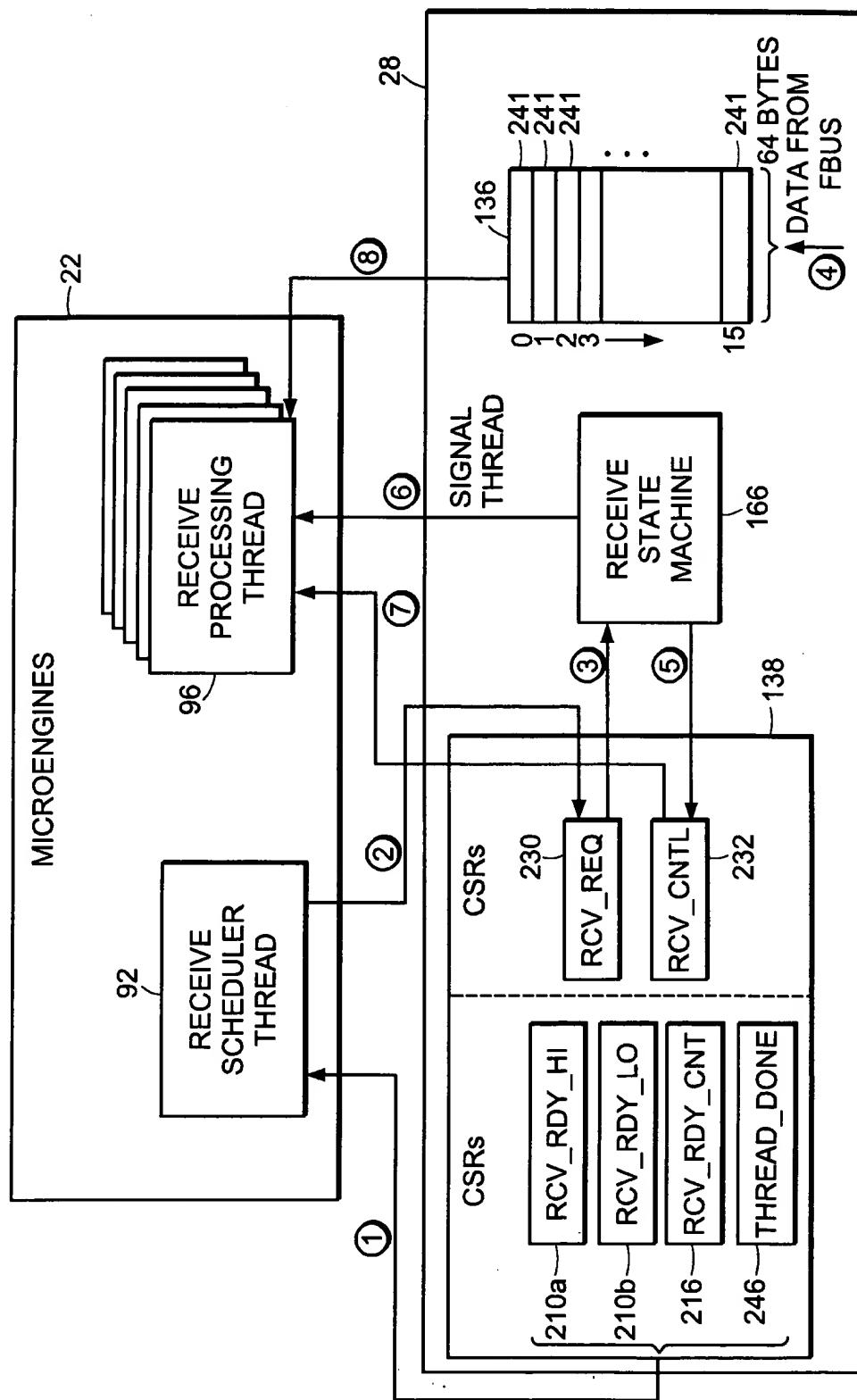


FIG. 9



230

230a	230b	230c	230d	230e	230f	230g	230h	230i	230j	230k	230l	230m
231	RES	FA		JL	E2	E1	FS	NFE	IG FR	STGRS	TID	RM
231												

FIG. 10A

232

232a	232b	232c	232d	232e	232f	232g	232h	232i	232j	232k	232l	232
233	THRSG	MACPORT/ THD	SOP/ SEQ#	RF	REAR	SE	FE	EFOR	SNOR	VALID BYTES	EOP	SOP
233												
233												

FIG. 10B

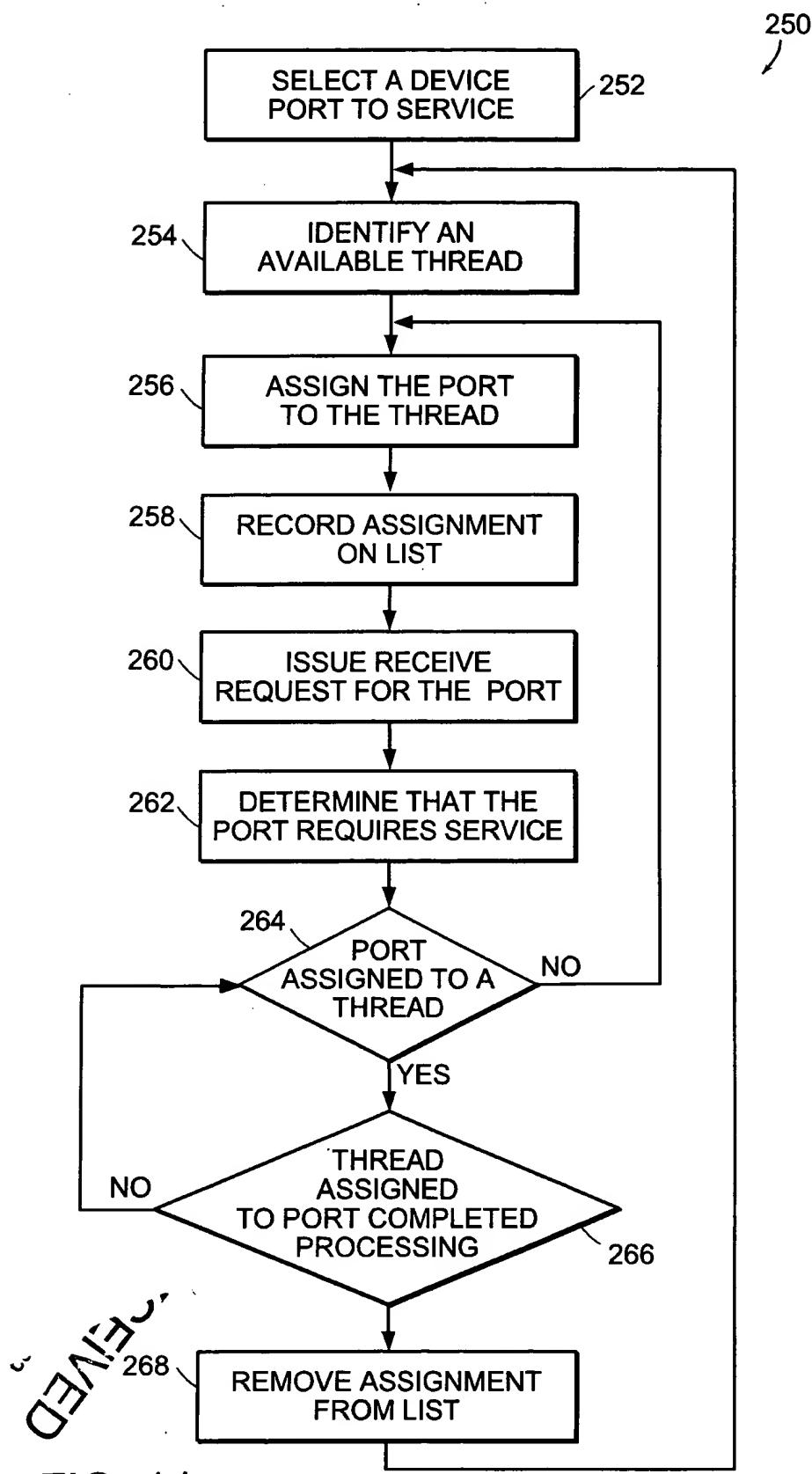


FIG. 11